

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)	
)	
Thomas R. Apel, et al.)	Examiner: Not Assigned
)	
Application No.: Not Assigned)	Art Unit: Not Assigned
)	
Filed: January 30, 2004)	
)	
For: BIPOLAR JUNCTION)	
TRANSISTOR GEOMETRY)	
_____)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1460

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

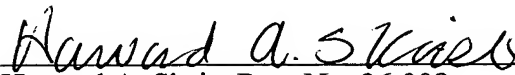
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Respectfully submitted,

BERKELEY LAW AND TECHNOLOGY GROUP

Dated: 1/30/04


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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	Not Assigned Yet
Filing Date	January 30, 2004
First Named Inventor	Thomas Apel, et al.
Art Unit	Not Assigned Yet
Examiner Name	Not Assigned Yet
Attorney Docket Number	008.P001

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	1	US- 6,586,782 B1	7/1/2003	Finlay	
	2	US- 6,627,925 B1	9/30/2003	Finlay	
	3	US-			
	4	US-			
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FOREIGN PATENT DOCUMENTS					
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		Application Number	Not Assigned Yet		
		Filing Date	January 30, 2004		
		First Named Inventor	Thomas Apel, et al.		
		Art Unit	Not Assigned Yet		
		Examiner Name	Not Assigned Yet		
Sheet	2	of	2	Attorney Docket Number	008.P001

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	3	WILLIAM LIU, "HANDBOOK OF III-V HETEROJUNCTION BIPOLAR TRANSISTORS", Copyright 1998 by John Wiley & Sons, Inc., 4 Pgs.	
	4	JOHNSON, ET AL., "BASE-COLLECTOR CAPACITANCE REDUCTION OF AlGaAs/GaAs HETEROJUNCTION BIPOLAR TRANSISTORS BY DEEP Ion IMPLANTATION"	
		June 1995, 20 Pgs.	
	5	HARRY F. COOKE, "MICROWAVE TRANSISTORS: THEORY AND DESIGN", IEEE, Vol. 59, August 1971, Pgs. 68-86.	
	6	CHEN, ET AL., "BIPOLAR MICROWAVE LINEAR POWER TRANSISTOR DESIGN", IEEE Transactions on Microwave Theory and Techniques. Vol. 27, No. 5, May 1979, 8 Pgs.	

Examiner Signature		Date Considered	
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